

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

System And Method Of Digitally Testing An Analog Driver Circuit

Application Number : 10/708,788

Confirmation Number:

First Named Applicant: Joseph Marsh

Attorney Docket Number: FIS920030349US1

Art Unit: 2829

Examiner: Roberto Velez

Search string: (4298980 or 5610530 or 5481471 or 5107208 or 6011387 or 5861774 or 6680681
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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
/RV/	1	4298980	1981-11-03	HAJDU, ET AL.			
/RV/	2	5610530	1997-03-11	WHETSEL			
/RV/	3	5481471	1996-01-02	NAGLESTAD, ET AL.			
/RV/	4	5107208	1992-04-21	LEE			
/RV/	5	6011387	2000-01-04	LEE			
/RV/	6	5861774	1999-01-19	BLUMENTHAL			
/RV/	7	6680681	2004-01-20	HSU, ET AL.			

Signature

Examiner Name	Date
/Roberto Velez/	07/17/2008